## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): GOTOU et al

Serial No.:

09/965,221

Filed:

September 28, 2001

For:

GROUP TOO TOO Method Of Manufacturing A Semiconductor Device

Group:

1732

Examiner:

C.Lattin

## **AMENDMENT**

**Assistant Commissioner for Patents** Washington, D.C. 20231

April 8, 2003

Sir:

In response to the Office Action mailed January 8, 2003, please amend the above-identified application as follows:

## IN THE CLAIMS:

Please cancel claims 1-28 without prejudice or disclaimer, and add the following new claims to the application:

--29. A method of manufacturing a semiconductor device, comprising the steps of:

providing a substrate having a main surface, a back surface opposite the main surface, a plurality of product forming areas arranged in a matrix, with spaces therebetween, on the main surface and connecting electrodes formed on each of the plurality of product forming areas;